

ABSTRACT OF THE DISCLOSURE

A thin-film transistor liquid crystal display (TFT-LCD) substrate mainly includes a substrate and a planarization layer thereon. The substrate is defined to form a thin-film transistor (TFT) and a contact plug thereon and the source/drain of the TFT is electrically coupled with the contact plug. The planarization layer is disposed on the substrate and the planarization layer has a via hole for penetrating the planarization layer to expose to the contact plug. The configuration of the cross-section of the via hole includes a straight edge so that the via hole is able to be formed with a less steep taper at a lateral view by reflow.

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